

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
IS9-017/SERIAL NO.  
09/904.578LIST OF ARTICLES BY APPLICANT  
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July 12, 2001GROUP  
2812

## U.S. PATENT DOCUMENTS

*Examiner/ Initial/		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
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	AH						
	AI						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

DF	AO		"Active Clamp Circuits for Switchmode Regulators Supplying Microprocessor loads" Sanders et al.; 5/1997; pps. 1179-1185.					
DF	AP		"An Active Clamp Circuit for Voltage Regulation Module (VRM) Applications"; Wu et al.; IEEE Transactions on Power Electronics, Vol. 16, No. 5; 9/2001; pps. 623-634					
DF	AQ		"An Integrated Controller for a High Frequency Buck Converter"; Lau et al.; 5/1997; pps. 246-254.					
DF	AR		"A Low-Voltage CMOS DC-DC Converter for a Portable Battery-Operated System"; Stratakos et al; 5/1994; pps. 619-626.					
DF	AS		"International Rectifier Introduces HexFET® Power MOS in FlipFET™ Packages Enabling World's Highest Power Density"; <a href="http://www.irf.com/whats-new/nr000612a.html">http://www.irf.com/whats-new/nr000612a.html</a> ; June 2000; 2 pps.					
DF	AT		U.S. Patent Application Pub. No. US 2001.0038277 A1; Publication Date 11/8/2001; Burnstein et al.					
DF	AU		U.S. Patent Application Pub. No. US 2001.0035746 A1; Publication Date 11/1/2001; Burnstein et al.					

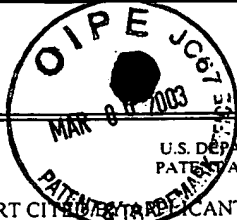
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PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
IS9-018/SERIAL NO.  
09/905, 187LIST OF ART CITATIONS  
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*Examiner/ Initial/		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
DF	AA	6,329,801 B1	12/11/01	Zuniga et al.			
DF	AB	6,278,264 B1	8/21/01	Burnstein et al.			
DF	AC	6,271,651 B1	8/7/01	Stratakos et al.			
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## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AO							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

DF	AR		"Synchronous Rectification with Adaptive Timing Control"; Acker et al.; 06/95; pps. 88-95.					
DF	AS		"High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications"; Stratakos; The 12th International Symposium on Power Semiconductor Devices & ICs; 1998; pps. 1-245.					
DF	AT		"A Low Voltage CMOS Compatible Power MOSFET for On-Chip Dynamic Voltage Scaling DC-DC Converters" Nassif-Khalil et al.; 1/2000; pps. 43-46					
DF	AU		"FlipFET™ MOSFET Design for High Volume SMT Assembly"; Schofield et al.; <a href="http://www.irf.com/technical-info/papers.html">http://www.irf.com/technical-info/papers.html</a> ; 3/18/2001; 6 pps.					

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